OFFICIAL

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Serge Lasserre (TI-31354)   Conf. No.: 4434

Serial No: 09/932,381   Examiner: Reginald G. Bragdon

Filed: August 17, 2001   Art Unit: 2188

For: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access

AMENDMENT

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

<u>Introductory Comments:</u>

This paper is presented in response to the Office Action mailed on April 26, 2004. It is submitted that this paper is fully responsive to that Office Action. Reconsideration of this application, based on the following amendment and accompanying remarks is respectfully requested.

Amendment to the claims begin on page 2.

Applicant's remarks begin on page 8.

To: Technology Center 2188
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From: **Rodney M. Anderson**
       Anderson, Levine, and Lintel
       (fax) 972.664.9606   RECEIVED
       (voice) 972.664.9554   CENTRAL FAX CENTER

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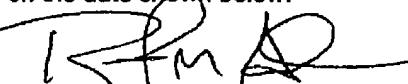
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CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by
facsimile to the U.S. Patent and Trademark Office at 703.872.9306
on the date shown below:

_____   July 20, 2004
Rodney M. Anderson   Date

FACSIMILE COVER SHEET

X FACSIMILE COVER SHEET	**X** AMENDMENT(12 Pages)
___ NEW APPLICATION	___ EOT ___
___ DECLARATION	___ NOTICE OF APPEAL
___ ASSIGNMENT	___ APPEAL ___
___ FORMAL DRAWINGS	___ ISSUE FEE
___ INFORMAL DRAWINGS	___
___ CONTINUATION APP'N	
___ DIVISIONAL APP'N	

Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75074